| Notice of Allowability | Application No. | Applicant(s) AKRAM, SALMAN | | |
|---|--|--|---------------------|--|
| | 09/652,495 | | | |
| | Examiner | Art Unit | | |
| | Nitin Parekh | 2811 | | |
| The MAILING DATE of this communication appeal All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI | (OR REMAINS) CLOSED in or other appropriate communication is s | n this application. If not includ unication will be mailed in due | led course. THIS | |
| 1. X This communication is responsive to 3-22-07. | | | | |
| 2. ☑ The allowed claim(s) is/are <u>1-3,5-41 and 43-55</u> . | | | | |
| Acknowledgment is made of a claim for foreign priority ur a) All b) Some* c) None of the: 1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 3. Copies of the certified copies of the priority documents have International Bureau (PCT Rule 17.2(a)). * Certified copies not received: | e been received. e been received in Application | on No | ition from the | |
| Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE. | | a reply complying with the re | quirements | |
| A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give | | | IOTICE OF | |
| 5. CORRECTED DRAWINGS (as "replacement sheets") mus | st be submitted. | | | |
| (a) I including changes required by the Notice of Draftspers | son's Patent Drawing Review | v (PTO-948) attached | | |
| 1) 🗌 hereto or 2) 🔲 to Paper No./Mail Date | | , | | |
| (b) ☐ including changes required by the attached Examiner's Paper No./Mail Date | s Amendment / Comment or | in the Office action of | • | |
| Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in t | | | e back) of | |
| DEPOSIT OF and/or INFORMATION about the depo attached Examiner's comment regarding REQUIREMENT | | | Note the | |
| | | | | |
| Attachment(s) | _ | | | |
| 1. Notice of References Cited (PTO-892) | | □ Notice of Informal Patent Application | | |
| 2. Notice of Draftperson's Patent Drawing Review (PTO-948) | 6. ☐ Interview St Paper No. | Interview Summary (PTO-413), Paper No./Mail Date | | |
| Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date | 7. Examiner's | Amendment/Comment | | |
| Examiner's Comment Regarding Requirement for Deposit of Biological Material | 8. 🛛 Examiner's Statement of Reasons for Allowance | | | |
| | 9. | - Nitri farell | - | |
| | · | NITIN PAREKH PRIMARY PATENT EY | | |

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DETAILED ACTION

Allowable Subject Matter

1. Claims 1-3, 5-41 and 43-55 are allowed.

Reasons for Allowance

2. The following is an examiner's statement of reasons for allowance:

The references of record do not teach the limitations "an interposer comprising a semiconductor material and including a first surface disposed adjacent the active surface and including: solder forming at least one electrically conductive via and a conductive structure protruding from the at least one electrically conductive via at the first surface of the interposer, positioned over the semiconductor device" and "at least one conductive trace in communication with the at least one electrically conductive via and carried on a second surface of the interposer, opposite from the first surface of the interposer" in a chip scale package having a device disposed on an interposer with conductive vias; or "an interposer comprising a semiconductor material and including solder forming at least one via through the interposer and a conductive structure protruding from the at least one via at a first end of the at least one via located proximate a first surface of the interposer and positioned to substantially align with a corresponding bond pad of a semiconductor device to be positioned adjacent to the first surface of the interposer" and "at least one conductive trace laterally extending from a second end of the at least one via and carried by a second surface of the interposer, which is located opposite the first surface of the interposer" in a flip chip carrier having a device disposed on an interposer with conductive vias.

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Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Nitin Parekh whose telephone number is 571-272-1663. The examiner can normally be reached on 09:00AM-05:30PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Rich Elms can be reached on 571-272-1869. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAN or Public PAG. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAG system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-308-0956.

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Nitin Parekh

PRIMARY EAMINER

TECHNOLOGY CENTER 2800

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